



### Company presentation

Fundiing for European Defence Research and Technology

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www.skylabs.si

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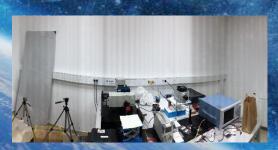
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### SkyLabs at glance

- Highly skilled electronics and software R&D engineering team
  - Team of 15 experts
  - **Embedded electronics and software development**
  - Analog electronics, radio systems, and signal processing
  - **Mechatronics development**
  - IP Cores (digital and mixed signals)
- **Engineering development approach** 
  - Miniaturization key aspect (following latest technology trends)
  - Hardware accelerated approach
  - Awareness of harsh space environment effect
- SkyLabs closely cooperates with University of Maribor **Laboratory for Electronic and Information Systems** 
  - Knowledge and technology transfer
  - **Recruiting of highly skilled professionals**
  - Core research capabilities (12 researchers)
  - TRISAT mission: First satellite developed with Slovenian knowhow









We are innovating the aerospace market with SkyLabs disruptive technologies, products and solutions to change the layout of space.





ThalesAlenia











Printech Circuit Laboratories







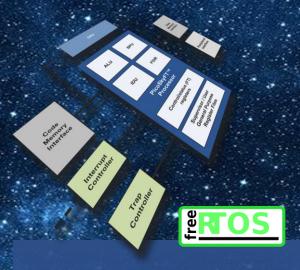






## PicoSkyFT soft-core processor

Small footprint, radiation hardened by design processor core



#### **Radiation testing campaigns:**

- PSI Proton beam up tp 230 MeV
- CERN UHE mono-energy. 40 GeV/n
- ChipIR Neutron beam 10-800MeV



#### PicoSkySIM™ & PicoSkyWARE

- Complete toolchain and debugger
- Clockexact simulator
- Interface board



**PicoSkyLINK** – programmer and debbuger

- Small footprint, soft-core and fault tolerant processor core.
- ESA activity (verification and radiation characterisation of the core)
- IP Core building block for true SoC architecture implementation and **technology independent**
- Architecture
  - RISC 8/16-bit Harvard architecture
  - Highly deterministic operation
  - Hard real time interrupt response capabilities
  - Low memory footprint of the application code
- Radiation hardened by design approach (SEE tolerant)
  - Fully distinguished dual operational mode (supervisor and user mode)
  - Spatial triplication (TMR) on register level (optional temporal redundancy)
  - EDAC protected memory blocks, by Hamming scheme
- FDIR policy for mitigation techniques



## PicoSkyFT Ecosystem

LCLs - Latching Current Limiters



- Immune to SEE
- Detect the overcurrent event
- Limit the current through the device to a safe level, preventing permanent damage due to overheating
- 3 level protection technique
  - o Component level
  - o Subsystem level
  - System level
- Enhanced telemetry on each system level



- o MIL-STD-1553B
- SpaceWire
- o CAN
- o SerDes,...



- SpaceWire
- o MIL-STD-1553B
- o CAN
- o RS422/232
- o SerDes,...

SKY-PICOSKY-EVAL [SKY9213]

- o CAN, PIO, USB
- o ADCs, DACs
- SPI / 12C



# NANOsky I - Satellite platform

Highly miniaturized & reliable nanoscale platform with FT features



The right solution for the emerging space market



Fault tolerant system design



**TRISAT** 

New type of harness



Pushing the limits of system miniaturization



Designed for high reliability and availability



Tailor made to clients needs

SEE immunity, innovative error mitigation techniques, sophisticated three-level FDIR policy, redundancy on all critical functions and thoughtful component selection ensure robustness, high reliability and availability of the platform



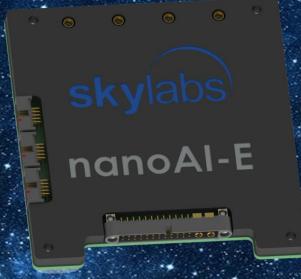
NANOimager Miniaturized multispectral SWIR imager

NANOobc
On-board computer

NANOlink S-Band SDR transceiver

NANOcomm UHF/VHF transceiver

NANOeps Electrical Power System NANOhpc-E — Al at the Edge



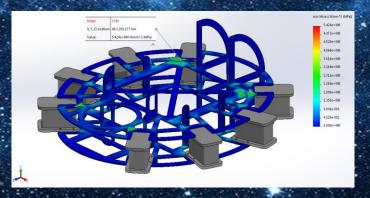
NANOai-E prototype







SARA Skylabs (Slovenian Arachnid Robot Adventurer)





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